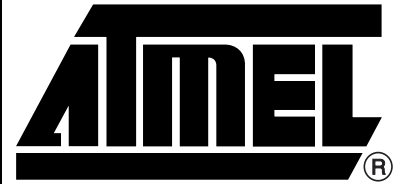

Atmel offers PLD design engineers a wide choice of full-featured electronic design software tools to fulfill different PLD design needs.

ProChip Designer[®] is a fully featured EDA software suite incorporating state-of-the-art synthesis and simulation tools from Mentor Graphics[®] and Altium[®] with Atmel's proprietary user interface/design navigator and device specific fitter software. Together, they support VHDL, Verilog[®], CUPL[®] and schematic designs for the Atmel ATF15xxAS/ASL/ASV/ASVL/BE Complex Programmable Logic Device (CPLDs). For more details, please refer to the "ProChip Designer Software Suite Overview" available online at www.atmel.com/dyn/products/other_docs.asp?family_id=653. An Evaluation Edition of ProChip Designer is available online at www.atmel.com/dyn/products/tools_card.asp?tool_id=2756.

The Altium Design Explorer[™] 99SE, one of the components in the ProChip Designer software suite, can also be used independently to create, compile and simulate schematic and CUPL designs for all Atmel SPLDs as well as the ATF750C/CL/LVC, ATF2500C, ATF1500A/AL, and ATF15xx CPLDs.

Atmel-WinCUPL supports CUPL designs for all Atmel Simple and Complex Programmable Logic Devices (SPLDs and CPLDs), which include the industry standard 16V8, 20V8, 22V10 SPLDs and ATF15xx, ATF750C, and ATF2500C CPLDs. This tool is free and available for download online at www.atmel.com/dyn/products/tools_card.asp?tool_id=2759.

Also included in both software suites are the latest generation Atmel device fitters which provide place-and-route with full support for Atmel Logic Doubling[®], on-line reference manual, tutorials, and help manual, plus web linked reference and assistance.



PLD Design Software Overview



	Feature	ProChip Designer	Atmel-WinCUPL
Design Entry	VHDL	x	
	Verilog	x	
	CUPL	x	x
	Schematic	x	
Synthesis	VHDL	x	
	Verilog	x	
	CUPL	x	x
	Schematic	x	
Simulation	VHDL (pre and post-fit simulation)	x	
	Verilog (pre and post-fit simulation)	x	
	CUPL (pre-fit simulation)	x	x
Fitter Technology	Atmel proprietary device fitters with Logic Doubling support	x	x
Devices	ATF15xxAS/ASL	x	x
	ATF15xxASV/ASVL	x	x
	ATF15xxBE	x	x ⁽¹⁾
	ATF1500A/AL	x ⁽²⁾	x
	ATF2500C	x ⁽²⁾	x
	SPLDs	x ⁽²⁾	x

- Notes:
1. ATF15xxBE support in WinCUPL is not available at press time. Please check with Atmel at pld@atmel.com for the most up-to-date information.
 2. Only schematic and CUPL design flows are supported by using Design Explorer 99SE from Altium independently.

As always, Atmel offers POF2JED conversion software that enables existing applications to acquire a true second source with little or no effort. POF2JED may be downloaded from the web, is more user-friendly than ever, has push-button EPM7xxx/3xxx supports and auto-configuration for 5V/3.3V, and POF2JED can still be used as a stand-alone application.

The Atmel ISP software, ATMISP, is used in conjunction with Atmel CPLD-ISP Cable or the ByteBlaster/MV/II download cable to program Atmel's JTAG ISP CPLDs (ATF15xxAS/ASL/ASV/ASVL and ATF15XXBE) on the target ISP hardware system. This software can also be used to generate the SVF files needed by the ATEs to program the ATF15xx's on the circuit boards.

To program the ATF15xxBE 1.8V CPLDs, ATMISP v6.0 or later and Atmel CPLD-ISP Cable v6.0 or later must be used.

Technical Support

Submit questions online at: <http://www.atmel.com/dyn/products/support.asp>

FAQ: http://www.atmel.com/dyn/products/tech_support.asp?Faq=y



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Literature Requests

www.atmel.com/literature

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