Warning:
1. To prevent system malfunctions during periods of insufficient power supply voltage, AT8xC51SND2C voltage supply shall be monitored by a voltage supervisor.
2. During USB operation, AT8xC51SND2C voltage supply shall be 3.0 V to 3.6V to comply USB specification.
3. AUDVBAT (Audio power amplifier supply) shall be connected to Battery supply (at least 3.2V).
Reset pin shall be connected to remote controller. Refer to Hardware guide FAQ for how to connect it.
During USB operation, AT8xC51SND2C voltage supply shall be 3.0 V to 3.6 V to comply with USB specification.

USB interface

RS232

UART interface

MMC/SD Interface

During USB operation, AT8xC51SND2C voltage supply shall be 3.0 V to 3.6 V to comply USB specification.
C36 and C38 can be replaced by 10u capacitors. This will decrease the frequency bandwidth. Refer to Hardware guide FAQ for full description.
NandFlash connection:
P3.6 : WR#
P3.7 : RD#
P2.0 : CLE
P2.1 : ALE
SCL: WP#
NandFlash WP# pin signal shall be pull down (47K ohms) and connected to AT8xC51SND2C SCL (from TWI\[1:0\] bus).
P2.3 : RDY/BUSY
P2.7 : CE#

TIPS

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For debugging purpose, it is recommended to add testpoints on the following signals:

- ALE
- RST
- UART RX and TX
- PSEN
Refer to Hardware Guide for Layout Rules

Changes

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# v3.1.0, 25 Nov, 2005
- Clarify CPU non connected pins

# v3.1.0, 27 May, 2005
- First release of AT83SN2DMP3 schematics based on AT89RF0-8 reference board.

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