Warning:
1. To prevent system malfunctions during periods of insufficient power supply voltage, AT8xC51SND2C voltage supply shall be monitored by a voltage supervisor.
2. During USB operation, AT8xC51SND2C voltage supply shall be 3.0 V to 3.6V to comply USB specification.
3. AUDVBAT (Audio power amplifier supply) shall be connected to Battery supply (at least 3.2V).
Reset pin shall be connected to remote controller.
Refer to Hardware guide FAQ for how to connect it.
During USB operation, AVDD/GND voltage supply shall be 3.3 V to 3.6V to comply USB specification.
P3.3 : functional name is START_USB SND2 input
P3.5 : functional name is START_ENUM SND2 output
C36 and C38 can be replaced by 10u capacitors. This will decrease the
frequency bandwidth. Refer to Hardware guide FAQ for full description.
Nand Flash connection:

- P3.6: WR#
- P3.7: RD#
- P2.0: CLE
- P2.1: ALE
- SCL: WP#. Nand Flash WP# pin signal shall be pull down (100K ohms) and connected to AT8xC51SND2C SCL (from TWI[1:0] bus).
- P2.3: RDY/BUSY
- P2.7: CE#

For debugging purpose, it is recommended to add test points on the following signals:
- ALE
- RST
- UART RX and TX
- PSEN

Refer to Hardware Guide for Layout Rules.