MIL-PRF-38535
CUSTOM MICROCIRCUIT CERTIFICATION

FOR
CLASS Q & V
IS HEREBY AWARDED TO

ATMEL NANTES S.A.
Bp 70602 la Chantrerie
44306 Nantes Cedex 3 France

ALL ASSEMBLY AND TEST SHALL BE IN ACCORDANCE WITH MIL-PRF-38535.
THIS CERTIFICATION IS ISSUED IN CONJUNCTION WITH DSXC LETTER VQ (VQC-03-00123) AND IS VALID UNTIL TERMINATED BY WRITTEN NOTIFICATION FROM DSXC.

[Signature]
ALAN S. THOMPSON
Rear Admiral, SC, USN
Commander
DEFENSE SUPPLY CENTER, COLUMBUS
SUBJECT: Level(s) Q and V Certification, MIL-PRF-38535, FSC 5962

Mr. Francois Faes
Managing Director European Quality
Atmel Nantes S.A.
BP 70602 la Chantrerie
44306 Nantes Cedex 3
France

Dear Mr. Faes:

Atmel Nantes has demonstrated to the Defense Supply Center, Columbus (DSCC) that it complies with MIL-PRF-38535, the performance specification used by the Department of Defense for Monolithic integrated circuits.

Atmel Nantes’ full Q and V level certification will continue for the technology flows used for the quality assurance levels listed in the enclosure. This certification and listing includes the subcontractors approved by Atmel Nantes. This letter supersedes DSCC letter (DSCC-VQ-VQC 00-014) to reflect the current certification status of Atmel Nantes.

In addition, the parts that are manufactured using the certified technology flows are being listed on the QML-38535. This will allow Atmel Nantes to mark parts with "Q" or "QML". These designators have been authorized by the Department of Defense for parts that have been produced to a QML specification, (i.e., one which allows less government oversight), the use of world-wide commercial production lines, reduced finished product testing based on statistical process controls (SPC), and other cost advantages.

Testing must be performed using the facilities and methods listed in the Laboratory Suitability letter (DSCC-VQC-03-003122), or at facilities approved by the Atmel Nantes Technical Review Board using its MIL-PRF-38535 Quality Management Program Plan.

This certification is subject to the conditions in DoD 4120.3-M, Defense Standardization Program.

Any and all of the facilities mentioned on the enclosure are subject to an audit by the Qualifying Activity at any time. The manufacturer shall be responsible for all audit expenses incurred for the offshore facilities. Offshore facilities are subject to all of the conditions of MIL-PRF-38535, Appendix E.
QPL/QML manufacturers shall notify the qualifying activity immediately after learning of a potential issuance of a GIDEP alert, problem advisory or major quality/reliability problem on their QPL/QML products. Failure to provide prior notification may be grounds for removal from QML-38535.

In addition, it is requested that the following activities be reported promptly to DSCC:

- Changes to certified facilities, process flows, or approved testing subcontractors
- Problem evaluation and a corrective action when:
  a. A Technology Conformance Inspection (TCI) failure has been validated
  b. The reliability of shipped parts is questionable
- Test optimization, including:
  b. Changing, suspending or canceling a prior test optimization
- Additions or deletions of parts in the QML-38535
- Change of company QML contact or other key QML personnel

This certification is valid until terminated by written notice from the Qualifying Activity. If warranted, it may be withdrawn by this Center at any time.

If you have any questions, please contact Mr. Andy Thacker at (614) 692-0589.

Sincerely,

L. DARRELL HILL
Chief
Sourcing and Qualification Unit

Enclosure

cc:
Atmel-Nantes (Ms. Monique Leseur)
VQC (Scott Thomas)
VQC (Michael Grammens)

Visit us on the web at: www.dssc.dla.mil/offices/sourcing and qualification/index.html
QM Program for MIL-PRF-38535 based on Atmel Nantes’ QM Plan

<table>
<thead>
<tr>
<th>OPERATION</th>
<th>LOCATION</th>
<th>LINE/FLOW</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fab</td>
<td>BP 70602 La Chantrerie</td>
<td>- CMOS 0.8µm</td>
</tr>
<tr>
<td></td>
<td>44306 Nantes Cedex 3 France</td>
<td>- CMOS 0.6µm</td>
</tr>
<tr>
<td></td>
<td></td>
<td>- CMOS 0.5µm</td>
</tr>
<tr>
<td></td>
<td>ATMEL ES2 Zone Industrielle</td>
<td>- CMOS 0.35µm</td>
</tr>
<tr>
<td></td>
<td>13106 Rousset Cedex France</td>
<td></td>
</tr>
<tr>
<td>Hermetic Assembly</td>
<td>Atmel Grenoble, France</td>
<td>CERQUAD, PGA, MLCC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Side Brazed, MQFP</td>
</tr>
<tr>
<td>Test</td>
<td>See Laboratory Suitability Number DSCC-VQC-03-003122</td>
<td></td>
</tr>
</tbody>
</table>